
 Search: ☒ The ACM Digital Library ☐ The Guide


 Searching within The **ACM Digital Library** with **Advanced Search**: (sha and hash and SIMD) ([start a new search](#))

Found 3 of 249,190

**REFINE YOUR SEARCH**
☒ Search Results

☐ Related Conferences

**Refine by Keywords**
  
 Discovered Terms 
**Refine by People**
[Names](#)  
[Institutions](#)  
[Authors](#)
**Refine by Publications**
[Publication Year](#)  
[Publication Names](#)  
[ACM Publications](#)  
[All Publications](#)  
[Publishers](#)
**Refine by Conferences**
[Sponsors](#)  
[Events](#)  
[Proceeding Series](#)
**ADVANCED SEARCH**

**FEEDBACK**


Found 3 of 249,190

Results 1 - 3 of 3

 Sort by  in 

**1 The Vector-Thread Architecture**


 June **ISCA '04**: Proceedings of the 31st annual international symposium on 2004 Computer architecture
**Publisher:** ACM
 Full text available:  (317.13 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)
**Bibliometrics:** Downloads (6 Weeks): 7, Downloads (12 Months): 75, Citation Count: 12

The vector-thread (VT) architectural paradigm unifies the vector and multithreaded compute models. The VT abstraction provides the programmer with a control processor and a vector of virtual processors (VPs). The control processor can use vector-fetch commands to ...

Also published in:

 March 2004 **SIGARCH Computer Architecture News** Volume 32 Issue 2

**2 Lx: a technology platform for customizable VLIW embedded processing**


 June **ISCA '00**: Proceedings of the 27th annual international symposium on 2000 Computer architecture
**Publisher:** ACM
 Full text available:  (344.41 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)
**Bibliometrics:** Downloads (6 Weeks): 9, Downloads (12 Months): 61, Citation Count: 80

Lx is a scalable and customizable VLIW processor technology platform designed by Hewlett-Packard and STMicroelectronics that allows variations in instruction issue width, the number and capabilities of structures and the processor instruction set. For ...

Also published in:

May 2000 **SIGARCH Computer Architecture News** Volume 28 Issue 2


**3** [Mapping irregular applications to DIVA, a PIM-based data-intensive architecture](#)



[Mary Hall](#), [Peter Kogge](#), [Jeff Koller](#), [Pedro Diniz](#), [Jacqueline Chame](#), [Jeff Draper](#), [Jeff LaCoss](#), [John Granacki](#), [Jay Brockman](#), [Apoorv Srivastava](#), [William Athas](#), [Vincent Freeh](#), [Jaewook Shin](#), [Joonseok Park](#)

January **Supercomputing '99**: Proceedings of the 1999 ACM/IEEE 1999 conference on Supercomputing (CDROM)

**Publisher:** ACM

Full text available:  Pdf (111.41 KB)

Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

**Bibliometrics:** Downloads (6 Weeks): 1, Downloads (12 Months): 22, Citation Count: 24

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2009 ACM, Inc.

[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)  [Real Player](#)